Hardware Resources in an Electronic Device

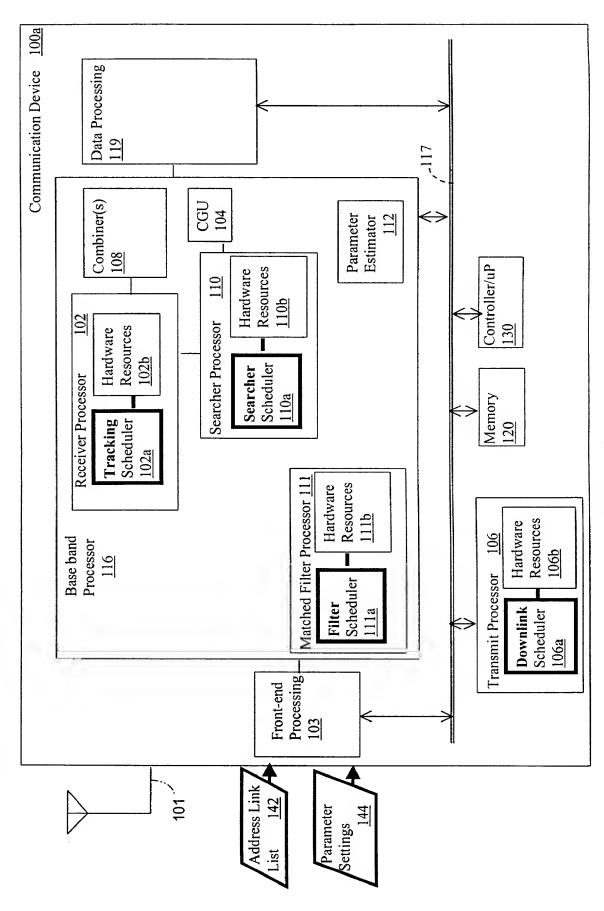


FIGURE 1A

Atty: Frank Morris
Atty Docket No: 009824-136-999 Inventor Kavoori Title: Wethod and Apparatus for Software-Based Allocation and Scheduling of Hardware Resources in an Electronic Device

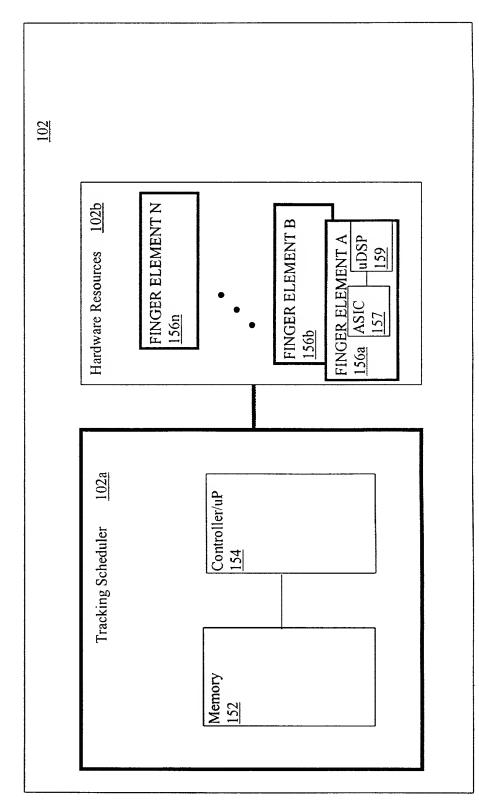


FIGURE 1B

Atty Docket No: 009824-136-999 Inventor Kavoori Title: Method and Apparatus for Software-Based Allocation and Scheduling of Tel No. 650-849-7777 Hardware Resources in an Electronic Device Atty: Frank Morris

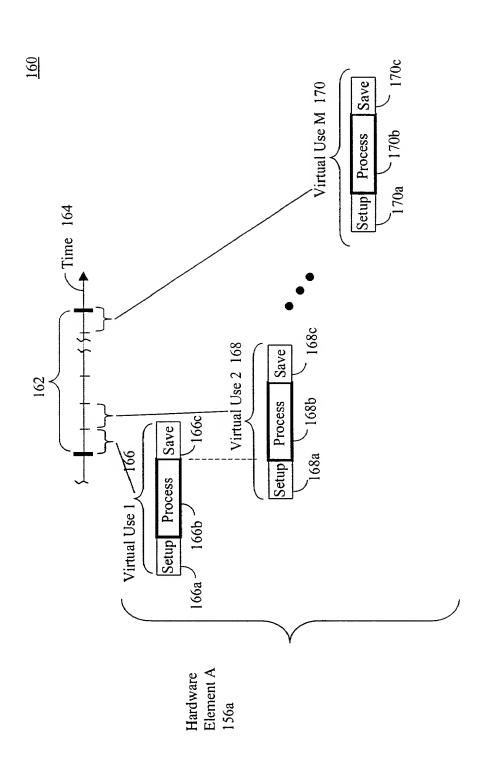


FIGURE 1C

Atty: Frank Morris

Tel No. 650-849-7777 LESTER TO TELLE TO TELLE

200A

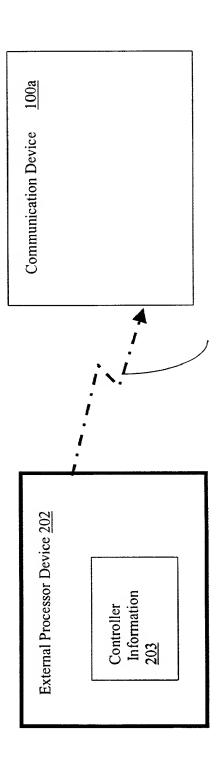


FIGURE 2A

Tel No. 650-849-7777 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 1979 (2017) 19

Atty Docket No: 009824-136-999 Inventor Kavoori Title: Method and Apparatus for Software-Based Allocation and Scheduling of Hardware Resources in an Electronic Device Atty: Frank Morris

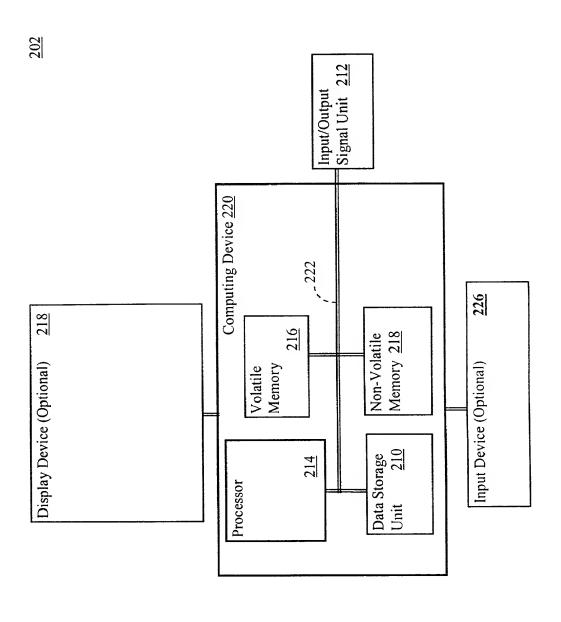


FIGURE 2B

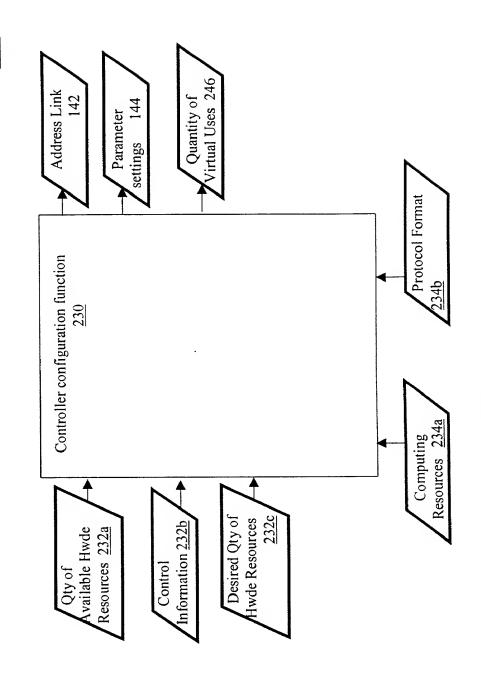


FIGURE 2C

DRAFT v 5 Atty: Frank Morris

Tel No. 650-849-7777
Atty Docekt No: 009824-136-999 Inventor 1: Rieken Title: Architecture For Dynamic Resource Allocation And Scheduling In A Communication Device

Express Mail No:

MTI CONFIDENTIAL

-ACTIVE Filest Address Link List **Address Link** 0010 1000 3101 0011 0001 0111 1001 1011 **List** 320 Second HW PARAM B1 HW PARAM B2 FIGURE 3A HW\_PARAM\_A2 HW\_PARAM\_B2 HW\_PARAM\_B2 HW\_PARAM\_B1 HW PARAM A1 0010 A HWHAMRAYBOB BI <del>[</del>] parameters set HWW BORRAM Link List 310 First Address 0010 1100 | 1 SW\_PARAM\_A2 SW\_PARAM\_B2 SW\_PARAM\_B1 SW\_PARAM\_B2 MWVIEGREGAT A1 BWS/MARRANG BI SW PARAM A1 SW PARAM BI SW PARAM B2 parameters set HW\_PARAM\_A1 parameters set by H/W 308 H/W control 314a 317 VilMual 304  $\mathbf{Z}$ Use 2 2 2 SW PARAM AI SW\_PARAM\_BT parameters set by S/W 306 H/W control Resource Pinental Bitzeck B Finger B Finger N inger N finger A Finger A Finger B Finger N 0010 -0100 0111 1000 1010 Virtual Use Resource Kinger A **Physical** Finger B 302 0010 0001

DRAFT v 5

Atty: Frank Morris Tel No. 650-849-7777

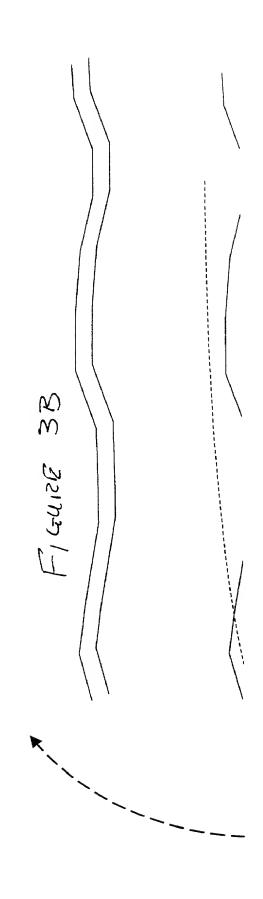
MTI CONFIDENTIAL

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Express Mail No:

A Communication Device

0100 { Finger N 1 SW_F 0111 { Finger B 2 SW_F 1000 { Finger B 2 SW_F	PARAM_B2 PARAM_A2 PARAM_B1	SW_PARAM_B2 HW_PARAM_B2 0111		0111
Finger A 2	PARAM_A2			
Finger B 2	PARAM BI	SW_PARAM_A2 HW_PARAM_A2 1000	1000	1000
	1	SW_PARAM_B1 HW_PARAM_B1 1001	1001	1010
1010 Finger N 2 SW_F	PARAM_B2	SW_PARAM_B2   HW_PARAM_B2   1011	1011	1011
1011 Finger A M SW_I	PARAM_A1	SW_PARAM_A1 HW_PARAM_A1 1100	1100	1100
Finger B M SW I	PARAM_B1	SW_PARAM_BI HW_PARAM_BI	1101	0001
Finger N M SW I	PARAM_B2	SW_PARAM_B2   HW_PARAM_B2   0001	0001	0001



1D 354c RESOURCE B TABLE 334 10-1 1 Start 354b 0001 0111 Group 354a ID TABLE 350 ID-N ID-2 ID-I  $\Box$ RESOURCE A TABLE 352 1D 352c 10-1 Start 352b 0001 0111 Group 352a ⋖

FIGURE 3C

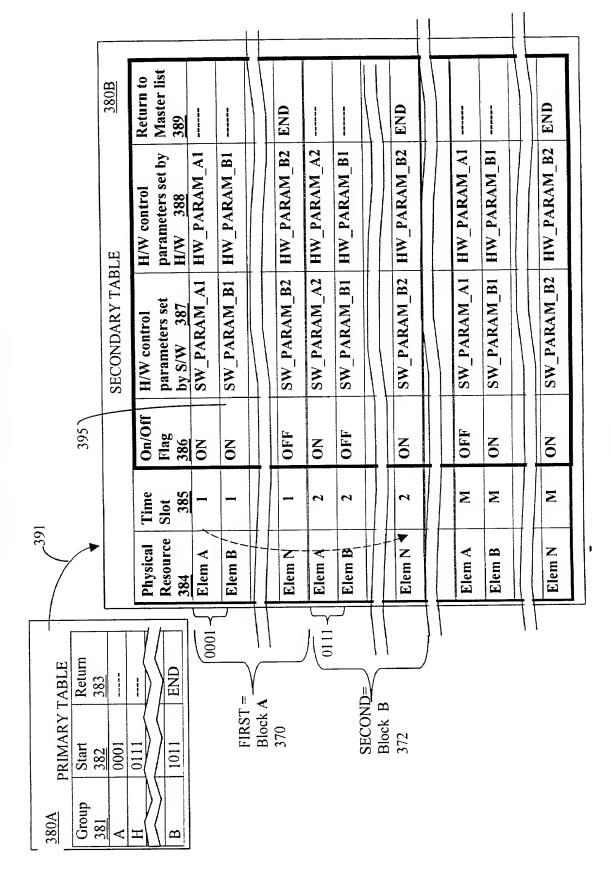


FIGURE 3D

Atty: Frank Morris Tel No. 650-849-7777

Atty Docket No: 009824-136-999 Inventor Kavoori et al. Title: Method and Apparatus for Software-Based Allocation and Scheduling of Hardware Resources in

an Electronic Device

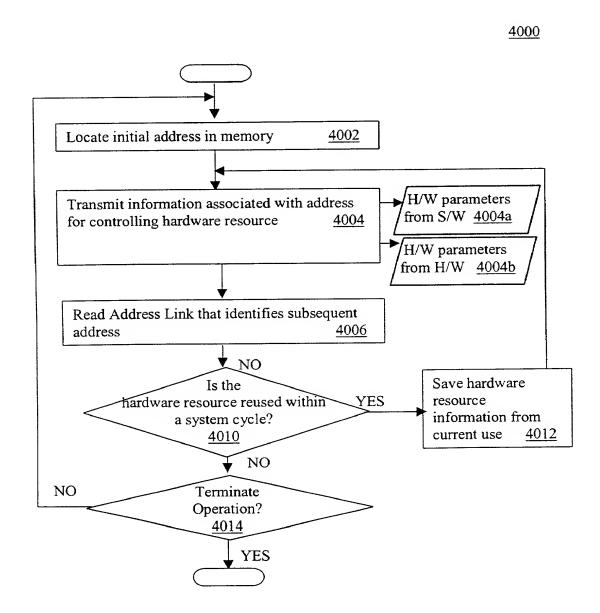


FIGURE 4A

Atty: Frank Morris

Tel No. 650-849-7777 Atty Docket No: 009824-136-999 Inventor Kavoori et al. Title: Method and Apparatus for Software-Based Allocation and Scheduling of Hardware Resources in an Electronic Device Receive a quantity of available hardware 4100 4102 resources Generate a list in memory for linking requests to hardware resource Receive desired quantity of hardware resources to be operated in target device H/W parameters set Receive operation information for operating the by S/W 4106a hardware resources Receive request to use hardware resource <u>4107</u> Assign a memory address to the control information for each of the hardware resources 4108 Associate with the Has the NO control information of a desired quantity of hardware given hardware resource, resources been linked? an address link to a 4110 subsequent hardware resource 4112 YES Generate control information of the last hardware resource 4114

FIGURE 4B

Atty: Frank Morris Tel No. 650-849-7777

Atty Docket No: 009824-13&999 Inventor Kavoori et al. Title: Method and Apparatus for Software-Based Allocation and Scheduling of Hardware Resources in

an Electronic Device

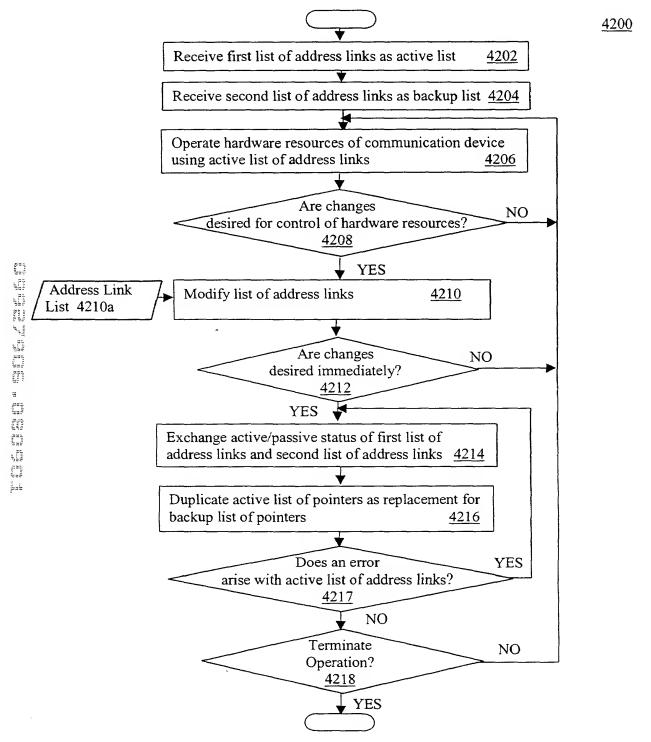


FIGURE 4C